ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

INTEGRATED CIRCUITS HAVING POST-SILICON ADJUSTMENT **CONTROL**

Submision Type: **Utility Patent Filing**

Application Number:

10/707450

52271

EFS ID: Server Response:

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First Named Applicant: Yoshiyuki Ando

Attorney Docket Number: **YA06**

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File Listing:

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Doc. Name	File Name	Size (Bytes) Date Produce	_
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us-fee-sheet	YA06-usfees.xml	1468 2003-12-	
us-fee-sheet	us-fee-sheet.xsl	24912 2003-12-	
us-fee-sheet	us-fee-sheet.dtd	11069 2003-12-	
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application-body	application-body.dtd	49498 2003-12-	
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application-body	Fig5.tif		5012	2003-12-15
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package-data	us-package-data.xsl		19263	2003-12-15
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